

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
25 September 2003 (25.09.2003)

PCT

(10) International Publication Number  
WO 03/079416 A1

(51) International Patent Classification<sup>7</sup>: H01L 21/00

(21) International Application Number: PCT/US03/08293

(22) International Filing Date: 17 March 2003 (17.03.2003)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data:  
60/364,653 15 March 2002 (15.03.2002) US  
10/140,140 7 May 2002 (07.05.2002) US

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(81) Designated States (*national*): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NI, NO, NZ, OM, PH, PL, PT, RO, RU, SD, SE, SG, SK, SL, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VN, YU, ZA, ZM, ZW.

(84) Designated States (*regional*): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IT, LU, MC, NL, PT, RO, SE, SI, SK, TR), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

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Published:

— with international search report

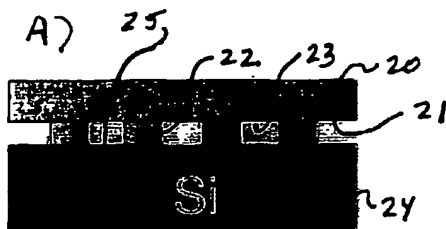
For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: LASER ASSISTED DIRECT IMPRINT LITHOGRAPHY

Quartz

Silicon

Molten Silicon



(57) Abstract: In accordance with the invention, features can be directly imprinted into the surface of a solid substrate. Specifically, a substrate is directly imprinted with a desired pattern by the steps of providing a mold (20) having a molding surface (21) to imprint the pattern, disposing the molding surface adjacent or against the substrate (24) surface (25) to be imprinted, and irradiating the substrate surface with radiation to soften or liquefy the surface. The molding surface is pressed into the softened or liquefied surface to directly imprint the substrate. The substrate can be any one of a wide variety of solid materials such as semiconductors, metals, or polymers. In a preferred embodiment the substrate is silicon, the laser is a UV laser, and the mold is transparent to the UV radiation to permit irradiation of the silicon workpiece through the transparent mold. Using this method, applicants have directly imprinted into silicon large area patterns with sub-10 nanometer resolution in sub-250 nanosecond processing time. The method can also be used with a flat molding surface to planarize the substrate.

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## **LASER ASSISTED DIRECT IMPRINT LITHOGRAPHY**

### **STATEMENT OF GOVERNMENT INTEREST**

This invention was made with government support under DARPA (N66001-98-1-8900) and ONR (N00014-01-1-0741). The government has certain rights to this invention.

### **CROSS REFERENCE TO RELATED APPLICATIONS**

This application claims the benefit of United States Provisional Patent Application 60/364,653 filed by Stephen Chou on March 15, 2002 and entitled "A Method for Direct Imprint of Microstructures in Materials".

This application is also a continuation-in-part of United States Patent Application Serial No. 10/244,276 filed by Stephen Chou on September 16, 2002 and entitled "Lithographic Method For Molding Pattern With Nanoscale Features" which, in turn, is a continuation of U.S. Application 10/046,594 filed by Stephen Chou on October 29, 2001, which claims priority to U.S. Patent Application Serial No. 09/107,006 filed by Stephen Chou on June 30, 1998 (now United States Patent No. 6,309,580 issued October 30, 2001) and which, in turn, claims priority to U.S. Application Serial No. 08/558,809 filed by Stephen Chou on November 15, 1995 (now U.S. Patent No. 5,772,905 issued June 30, 1998). All of the foregoing Related Applications are incorporated herein by reference.

This application is a continuation-in-part of United States Patent Application Serial No. 10/140,140 filed by Stephen Chou on May 7, 2002 and entitled "Fluid Pressure Imprint Lithography" which, in turn, is a Divisional of United States Patent Application Serial No. 09/618,174 filed by Stephen Chou on July 18, 2000 and entitled "Fluid Pressure Imprint Lithography" (now United States Patent No. 6,482,742 issued November 19, 2002).

### **FIELD OF THE INVENTION**

This invention relates to imprint lithography and, in particular, to laser assisted direct imprint lithography (LADI) wherein laser radiation permits direct imprinting of a mold on a substrate surface. The process is particularly useful in directly imprinting nanoscale features on solid substrates.

### **BACKGROUND OF THE INVENTION**

Methods of patterning small features onto substrates are of great importance in the fabrication of many electronic, magnetic, mechanical, and optical devices as well as devices for biological and chemical analysis. Such methods are used, for example, to define the features and configurations of microcircuits and the structure and operating features of planar optical waveguides and associated optical devices.

Optical lithography is the conventional method of patterning such features. A thin layer of photoresist is applied to the substrate surface and selected portions of the resist are exposed to a pattern of light. The resist is then developed to reveal a desired pattern of exposed substrate for further processing such as etching. A difficulty with this process is that resolution is limited by the wavelength of the light, scattering in the resist and substrate, and the thickness and properties of the resist. As a consequence optical lithography becomes increasingly difficult as desired feature size becomes smaller. Moreover applying, developing and removing resists are relatively slow steps, limiting the speed of throughput.

A more recent approach to patterning small features is nanoimprint lithography wherein a nanofeatured molding surface is imprinted into a resist or resist-like material (polymer) and the imprinted pattern is removed to selectively expose the substrate surface. This process eliminates the limitations on resolution caused by the wavelength of light, but nonetheless has limitations imposed by the use and processing of resists or polymers. Further details concerning nanoimprint lithography are set forth in applicant's United States Patent No. 5,772,905 issued June 30, 1998 and entitled "Nanoimprint Lithography" and U.S. Patent 6,482,742 issued November 19, 2002 and entitled "Fluid Pressure Imprint Lithography". The '905 and '742 patents are incorporated herein by reference.

The electronics and optical communications industries, as well as companies entering new fields of nanotechnology, continue to seek new methods for the production of microcircuits, optical devices and new nanoscale structures. In particular they seek faster processes for patterning substrates with higher resolution patterns.

### **SUMMARY OF THE INVENTION**

In accordance with the invention, features can be directly imprinted into the surface of a solid substrate. Specifically, a substrate is directly imprinted with a desired pattern by the steps of providing a mold having a molding surface to imprint the pattern, disposing the molding surface adjacent or against the substrate surface to be imprinted, and irradiating the

substrate surface with radiation to soften or liquefy the surface. The molding surface is pressed into the softened or liquefied surface to directly imprint the substrate. The substrate can be any one of a wide variety of solid materials such as semiconductors, metals, or polymers. In a preferred embodiment the substrate is silicon, the laser is a UV laser, and the mold is transparent to the UV radiation to permit irradiation of the silicon workpiece through the transparent mold. Using this method, applicants have directly imprinted into silicon large area patterns with sub-10 nanometer resolution in sub-250 nanosecond processing time. The method can also be used with a flat molding surface to planarize the substrate.

### **BRIEF DESCRIPTION OF THE DRAWINGS**

The advantages, nature and various additional features of the invention will appear more fully upon consideration of the illustrative embodiments shown in the accompanying drawings described in detail below. In the drawings:

Fig. 1 is a schematic flow diagram showing the steps involved in laser assisted direct imprint lithography;

Fig. 2A - 2E show the mold and substrate at various stages of the Fig. 1 process;

Fig. 3 is a graph plotting the reflectivity of a HeNe laser beam from the silicon surface as a function of the time when the silicon surface is irradiated;

Fig. 4 illustrates a scanning electron micrograph of an imprinted silicon grating;

Fig. 5(a) is a scanning electron micrograph of a cross-sectional view of a quartz mold;

Fig. 5(b) is a scanning electron micrograph of a cross-sectional view of a silicon substrate imprinted by the method of Fig. 1;

Fig. 6 is an atomic force micrograph of an isolated silicon square pattern produced by the method of Fig. 1;

Fig. 7 is a scanning electron micrograph of a quartz mold following two applications of the method of Fig. 1.

It is to be understood that these drawings are for the purposes of illustrating the concepts of the invention and, except for the graph, are not to scale.

### **DETAILED DESCRIPTION**

Referring to the drawings, Fig. 1 is a schematic block diagram showing the steps involved in laser assisted direct imprinting a desired pattern into the surface of a substrate without the interposition of a resist or polymer material. The first step shown in Block A is to provide a mold having a molding surface to imprint the desired pattern.

The next step, Block B, is to dispose the mold with the molding surface adjacent the substrate surface to be imprinted. The molding surface can be pressed against the substrate surface.

Fig. 2A shows the mold 20 having a molding surface 21 comprising one or more projecting features 22 and one or more recessed regions 23. The mold 20 is disposed near a substrate 24 having a surface 25 to be imprinted. The molding surface 21 is adjacent the surface 25.

The substrate 24 can be any material that will soften or liquefy in response to laser radiation and return to its original state when the laser exposure is terminated. The mold 20 is advantageously composed of a material transparent to the laser radiation so that the substrate surface 25 can be irradiated through the mold.

The molding surface 21 can have microscale or nanoscale features. Advantageously the surface comprises a nanofeatured pattern having at least one feature with a minimum dimension of less than 200 nanometers, for example the lateral dimension of a projecting feature 22 can be less than 200 nanometers. Also advantageously, the molding surface presents a mold depth of less than 250 nanometers, i.e. the distance between the outer portion of a projecting feature 22 and the inner portion of an adjacent recessed region 23 can be less than 250 nanometers and preferably in the range 5 - 250 nanometers. For planarizing the substrate surface, however, the molding surface is advantageously flat.

In a preferred embodiment, the substrate is silicon, the laser is a UV laser, and the mold is a fused quartz. A typical mold can be composed of 1 millimeter thick fused quartz having patterns with features ranging in size from 10 nanometers or less to tens of microns. Other advantageous substrates include other semiconductors, metals, semimetals, polymers and ceramics.

The substrate surface layer to be molded can be a multilayered structure of thin films of material selected among semiconductors, metals, semimetals, polymers and insulators. The surface layer can also be composed of plural surface regions, different surface regions formed of different materials.

The third step (Block C) is to irradiate the substrate surface to be molded with radiation to soften or liquefy the surface.

Fig. 2B illustrates a preferred arrangement for irradiation wherein radiation 26 from a source (not shown) passes through the mold 20 to irradiate surface 25 of the substrate. The laser radiation, which can be laser radiation, forms a softened or liquefied surface region 27 on the substrate 24.

Useful radiation sources include lasers (ultraviolet, infrared and visible light) and lamps (narrow band and broad spectrum). Laser radiation is advantageously in the range 1 nanometer to 100 micrometers. Lamp radiation is advantageously in the range 1 nanometer to 50 micrometers. The irradiation can be pulsed, advantageously with a time duration in the range 1 nanosecond to 10 seconds.

The next step shown in Block D of Fig. 1 is to imprint the substrate by pressing the molding surface into the softened or liquefied surface region.

This pressing can be accomplished by a mechanical press as described in the aforementioned U.S. Patent No. 5,772,905, by fluid pressure as described in U.S. Patent No. 6,482,742 or by any other method of applying force. The pressure can be applied beginning prior to the irradiation. One advantageous approach is to press the mold against the substrate and, during the pressing, irradiate the substrate surface with a plurality of pulses.

Fig. 2C shows the substrate with the molding surface 21 imprinting the surface region 27.

To complete the imprinting process, the irradiation is terminated as by switching off the source, the surface region is permitted to cool slightly, and the molding surface is removed from the substrate.

Fig. 2D illustrates the mold 20 removed from the substrate 24, leaving the imprinted mold pattern 28 on the substrate surface 25.

The invention may now be more clearly understood by consideration of the following specific examples:

### **Example**

A series of experiments were conducted using molds having the following three patterns: (1) grating of 140 linewidth, 110 nm depth, and 300 nm period; (2) lines 10 nm wide and 15 nm deep, which were created due to the trenching effect in reactive ion etching during mold fabrication; and (3) rectangles of length and width in tens of microns and a depth of 110 nm. All three patterns were imprinted directly in silicon in the same LADI process.

First, a quartz mold, as described above, was brought into contact with a silicon substrate. Two large press plates were used to supply the force which pressed the mold against the substrate. The silicon wafer was placed on the lower plate with the mold on top of the wafer. The top plate, also made of fused quartz and hence transparent to the laser beam, was placed on top of the mold. The two plates were pressed together by increasing the pressure provided by screws between the two large plates. The preferred level of pressure applied to bring the mold and the substrate into contact was approximately  $1.7 \times 10^6$  Pa or about 17 atmospheres pressure. This pressure level produces an imprint depth of ~110 nm in an imprint time of ~250 ns. It is preferable to have the pressure applied before introduction of the laser pulse.

Next, a laser pulse was applied to the mold-substrate assembly. In one experiment, a single XeCl excimer laser pulse having a wavelength of 308 nm, a pulse duration of 20 ns at FWHM, and a fluence of  $1.6 \text{ J/cm}^2$ , was applied to the assembly. The laser passed through the quartz mold with very little energy absorbed by the mold. The transmittance of the mold for the laser radiation was measured and found it to be ~93%. The difference in refractive index at the two mold-air interfaces caused a total radiation reflection of ~7% indicating the mold itself does not absorb the laser radiation.

The single laser pulse irradiates the silicon surface, inducing the melting and liquefying the silicon for approximately 250 ns. Advantageously, the melting of the substrate surface can be monitored *in situ* during the LADI process by measuring the time-resolved

reflectivity of a laser beam from the substrate surface. Reflectivity is the ratio of the energy of a wave reflected from a surface to the energy possessed by the wave striking the surface. When silicon melts, it changes from a semiconductor to a metal and the silicon's surface reflectivity to visible light increases by a factor of two.

In one experiment, a HeNe laser beam having a wavelength of 633 nm was used to measure the reflectivity. Fig. 3 shows the measured reflectivity of a silicon surface exposed to a single 20 ns XeCl excimer laser pulse at a fluence of  $1.6 \text{ J/cm}^2$ . As shown by the graph, the reflectivity of solid silicon is approximately .25. Under the radiation, the reflectivity of the silicon surface increased rapidly in the first 25 ns, saturated for 200 ns at a reflectivity of approximately .50, and returned to the original solid silicon reflectivity in 50 ns.

While the substrate was in a liquid state the mold was embossed into the liquid substrate layer. The low viscosity of molten silicon enables the molten silicon to flow rapidly into all crevasses, filling them completely and conforming to the mold. The melting period of the silicon is approximately equivalent to 20 ns, the duration of the laser pulse. In Fig. 3, the 25 ns raising edge of the reflectivity, which is slightly longer than the pulse duration, might be related to the RC time constant of the oscilloscope (100 MHz), which is 10 ns.

The mold and the substrate were then separated, leaving a negative profile of the mold pattern imprinted on the substrate surface. Typically, the transformation from liquid to solid causes the substrate volume to expand about 3% in each direction which may aid the conformation of the substrate to the mold features for achieving sub-10 nm resolution. It is desirable to separate the substrate and the mold easily, so not to damage either the imprinted features in the substrate or the features of the mold. Experimentation shows the separation is aided by the fact that the quartz mold has a lower coefficient of thermal expansion ( $5 \times 10^{-7} \text{ K}^{-1}$ ) than silicon ( $2.5 \times 10^{-6} \text{ K}^{-1}$ ).

Fig. 5(A) is a scanning electron micrograph showing a cross-sectional view of a quartz mold. Fig. 4 and Fig. 5 (B) are scanning electron micrographs showing a top and cross-sectional view, respectively, of the features imprinted in a silicon substrate as a result of LADI. The imprinted grating resulting from LADI has a 140 nm linewidth, 110 nm depth and 300 nm period, which is consistent with the features of the mold. One of the most



interesting features in Fig. 5(B) is the ridge formed along the top corners on the imprinted grating lines. These ridges are approximately 10 nm wide and 15 nm high. A comparison to the scanning electron micrograph of the mold in Fig. 5(A) clearly indicates that these ridges come from the notches formed in the mold caused by the trenching effect of the reactive ion etching of the mold. The complete transfer of these 10 nm ridges in a LADI process suggests that the resolution of LADI is better than 10 nm.

LADI also results in the imprinting of large, isolated patterns. Typically, LADI can pattern mesas and trenches of over tens of microns in size. Fig. 6 shows a silicon substrate imprinted by LADI with an isolated square mesa having a width of 8  $\mu\text{m}$  and height of 110 nm. The successful imprinting of such large patterns indicates that the molten silicon can easily flow over tens of microns within nanoseconds.

Fig. 7 shows a scanning electron micrograph of a quartz mold following two applications of LADI. Notably, the mold shows no visible damage due to the multiple LADI applications.

While UV laser radiation sources are advantageous, depending on the substrate other sources can be used. Infrared lasers can be used to soften or liquefy many metals and even heat lamps can be used to melt or soften many polymers.

If the surface to be imprinted comprises a plurality of layers, a combination of layers can be imprinted at the same time. The radiation can be chosen to melt a layer of material that can melt, through heat transfer, adjacent layers.

The pulse duration of the radiation can be selected upon the imprint process and the pulse can be short enough so that only the surface layer of the substrate being heated and the rest of the substrate as well as the mold will not be heated significantly.

It is also possible that there are pre-fabricated patterns on the substrate and there is alignment between the pattern on the substrate and the pattern on the mold.

The equipment for laser assisted direct imprint can have alignment capabilities comprising stages for the substrates and mold respectively and also the mechanical stage to create the relative movement between the substrates and the mold in x, y, z, zeta, tilt and yank (all possible degrees of freedom). The sensors can monitor the relative movement

between the substrates and the mold as well as monitoring the imprint heating pressure and information of the imprint controller would have monitoring feedback and control system.

It is contemplated that the above-described method can be used in a series of steps to form complex structures. A substrate can have an existing pattern, and the pattern on the mold can be aligned in relation to the existing substrate pattern before imprinting. A substrate can be patterned as described, a layer can be added and planarized if necessary, and the added layer can be patterned in the same manner but in a different configuration. Multiple successive layers can be imprinted with aligned patterns to form complex devices in the same manner that integrated circuits are formed. The substrate can comprise a thin layer of radiation absorbing material on a substrate transparent to the radiation. This method can be used with a polymer coated substrate to imprint a pattern into resists and then use the patterned resist to pattern the substrates as described in U.S. Patent No. 5,772,905.

Moreover the process can be used to make many devices on the same substrate using step and repeat imprinting and automatic alignment techniques well known in the art.

The imprinted substrates can have recessed regions that will hold patterns of material added after the imprinting. The added material can be magnetic or conductive to form tiny electromagnetic components. The recessed regions can even act as microscale or nanoscale molds for the fabrication of tiny components.

Microscale or nanoscale grooves can be imprinted into the substrate to select, orient and direct macromolecules, and imprinted peaks or recesses can be provided for attachment of indicators that will selectively attach to target molecules.

It is to be understood that the above described embodiments are illustrative of only a few of the many embodiments which can represent applications of the invention. Numerous and varied other arrangements can be made by those skilled in the art without departing from the spirit and scope of the invention.

What is claimed is:

1. A method of directly imprinting a mold pattern on the surface of a substrate comprising the steps of:

providing a mold having a molding surface to imprint the pattern;

disposing the mold near the substrate with the molding surface adjacent the substrate surface to be imprinted;

irradiating the substrate surface with radiation to soften or liquefy the substrate surface;

pressing the molding surface into the softened or liquefied substrate surface; and

removing the molding surface from the substrate to leave the substrate with the imprinted pattern of the molding surface.

2. The method of claim 1 wherein the radiation is laser radiation.

3. The method of claim 1 wherein the laser radiation is of wavelength in range 1 nanometer to 100 micrometers.

4. The method of claim 1 wherein the radiation is lamp radiation.

5. The method of claim 4 wherein the lamp radiation is predominantly in the range 1 nanometer to 50 micrometers.

6. The method of claim 1 wherein the irradiation is pulsed.

7. The method of claim 6 wherein the irradiation is pulsed with a time duration in the range 1 nanosecond to 10 seconds.

8. The method of claim 6 wherein the irradiation is pulsed repeatedly while the mold is pressed against the substrate.

9. The method of claim 1 wherein the radiation heats a surface layer of the substrate without substantially heating the mold or the entire substrate.

10. The method of claim 1 wherein the mold comprises material transparent to the radiation.

11. The method of claim 1 wherein the mold comprises fused quartz.

12. The method of claim 1 wherein the molding surface comprises a pattern of projecting and recessed features having at least one feature with a minimum dimension of less than 200 nanometers.

13. The method of claim 1 wherein the molding surface comprises a pattern of projecting and recessed features having a mold depth of less than 250 nanometers from the outer portion of a projecting feature to the inner portion of an adjacent recessed region.

14. The method of claim 1 wherein the substrate surface is irradiated with ultraviolet laser radiation.

15. The method of claim 1 wherein the molding surface is pressed into the substrate surface by a mechanical press or by fluid pressure.

16. The method of claim 1 wherein the molding surface is flat to planarize the surface of the substrate.

17. The method of claim 1 wherein the mold is formed of material transparent to ultraviolet radiation, the substrate is silicon, and the substrate is irradiated with ultraviolet laser radiation through the mold.

18. The method of claim 1 wherein the substrate surface comprises a semiconductor material.

19. The method of claim 1 wherein the substrate surface comprises a metal or alloy.

20. The method of claim 1 wherein the substrate surface comprises a polymer.

21. The method of claim 1 wherein the substrate surface comprises a ceramic.

22. The method of claim 1 wherein the substrate surface comprises a plurality of layers of materials to be molded.

23. The method of claim 1 wherein the radiation is infrared radiation.

24. The method of claim 1 wherein the substrate comprises a previously imprinted layer.

25. The method of claim 1 further comprising the step of filling with material the recessed regions of the imprinted pattern.

26. The method of claim 1 further comprising the step of repeating the imprinting process at another location on the same substrate.

27. The method of claim 1 wherein the substrate comprises an existing pattern and further comprising the step of aligning the pattern on the mold to the existing pattern on the substrate.

28. The method of claim 1 further comprising the step of planarizing the substrate prior to imprinting the substrate.

FIG. 1

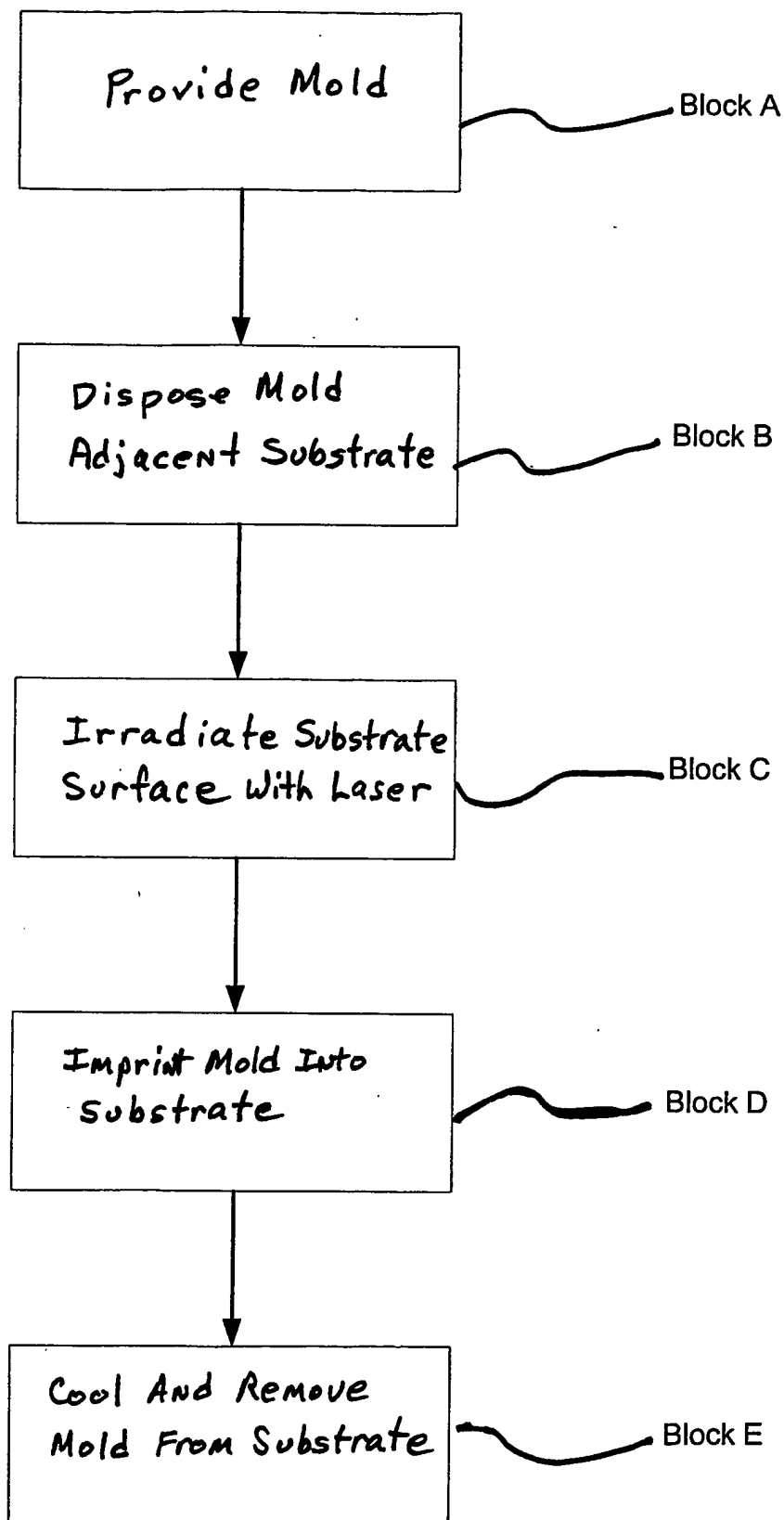


Fig. 2



Quartz



Silicon



Molten Silicon

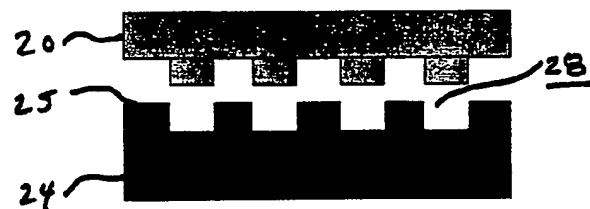
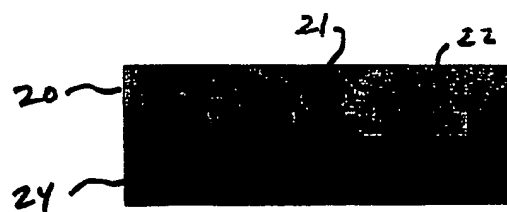
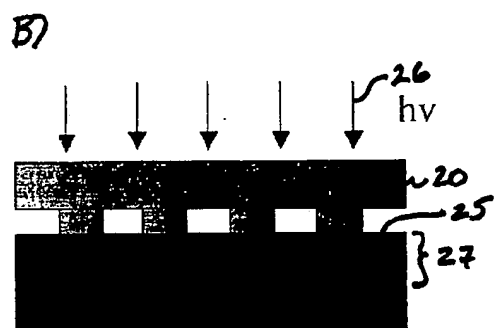
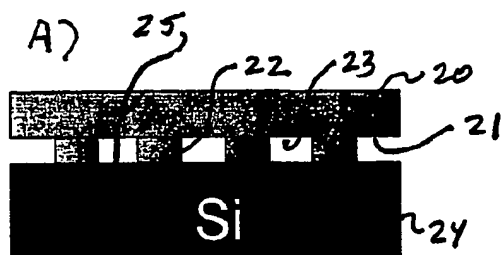




Fig. 3

### Time Resolved Reflectivity

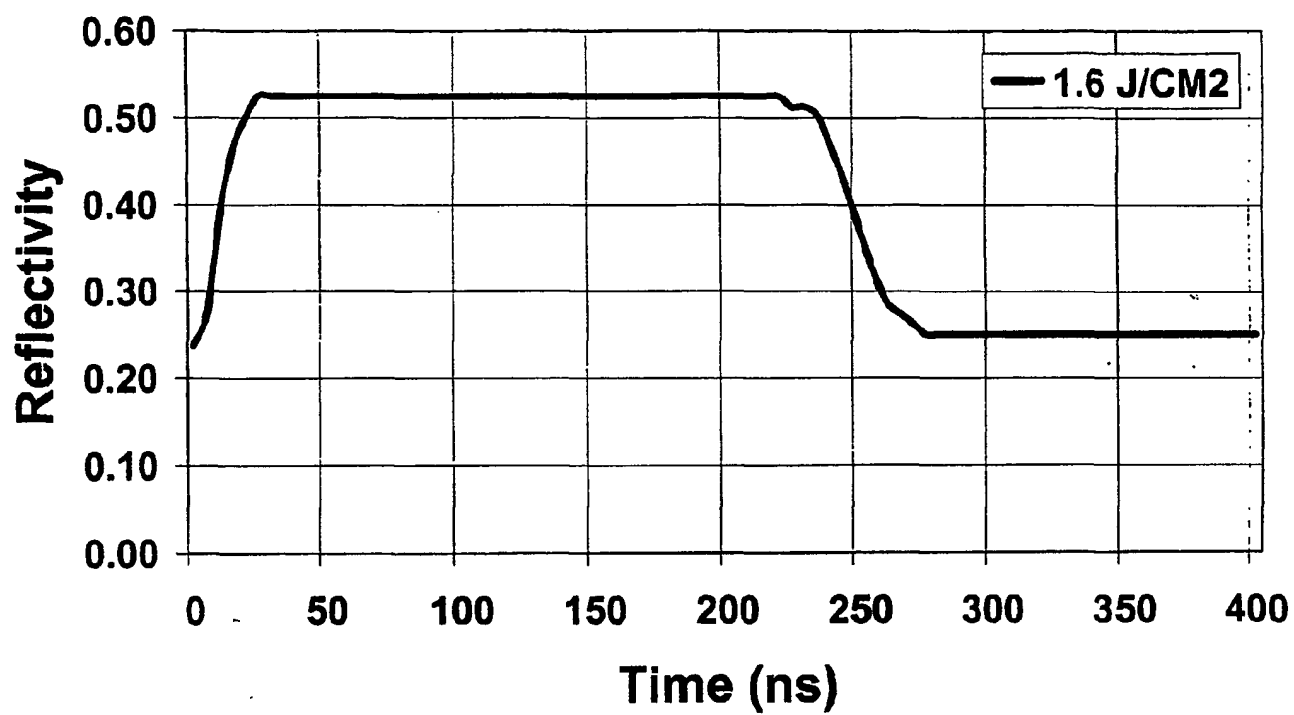


Fig. 4

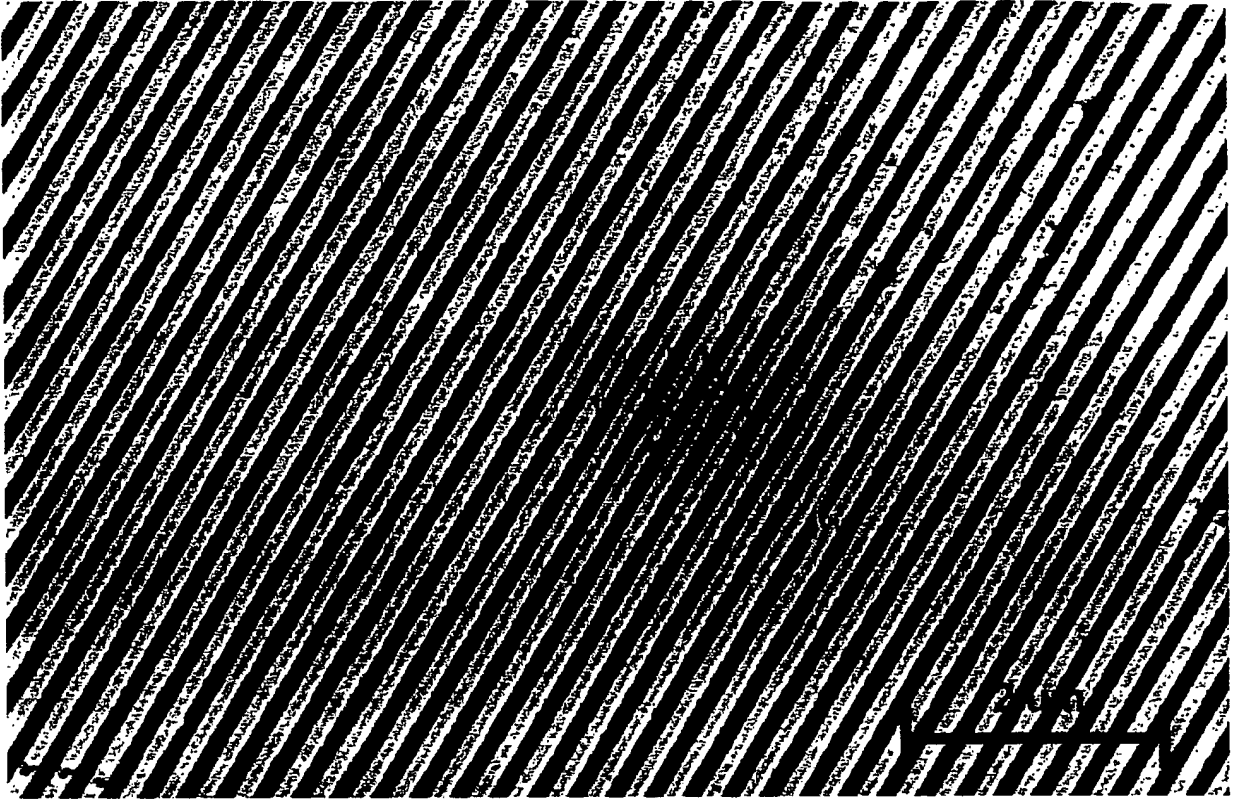


Fig. 5

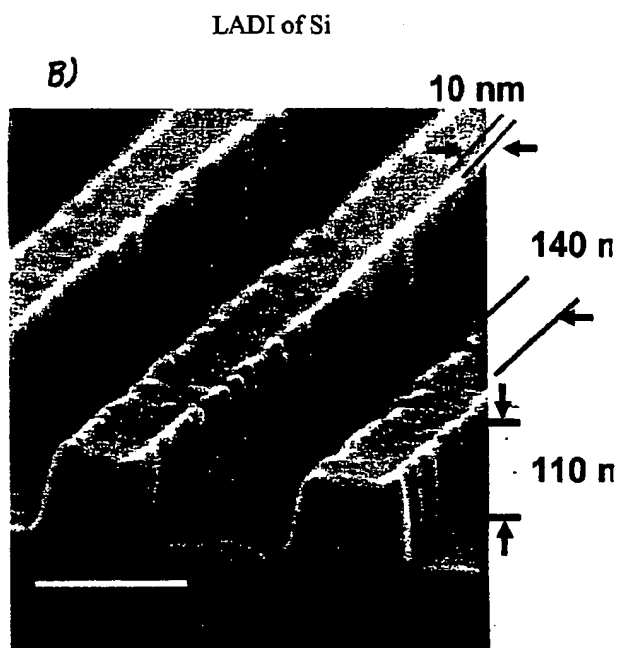


Fig. 6

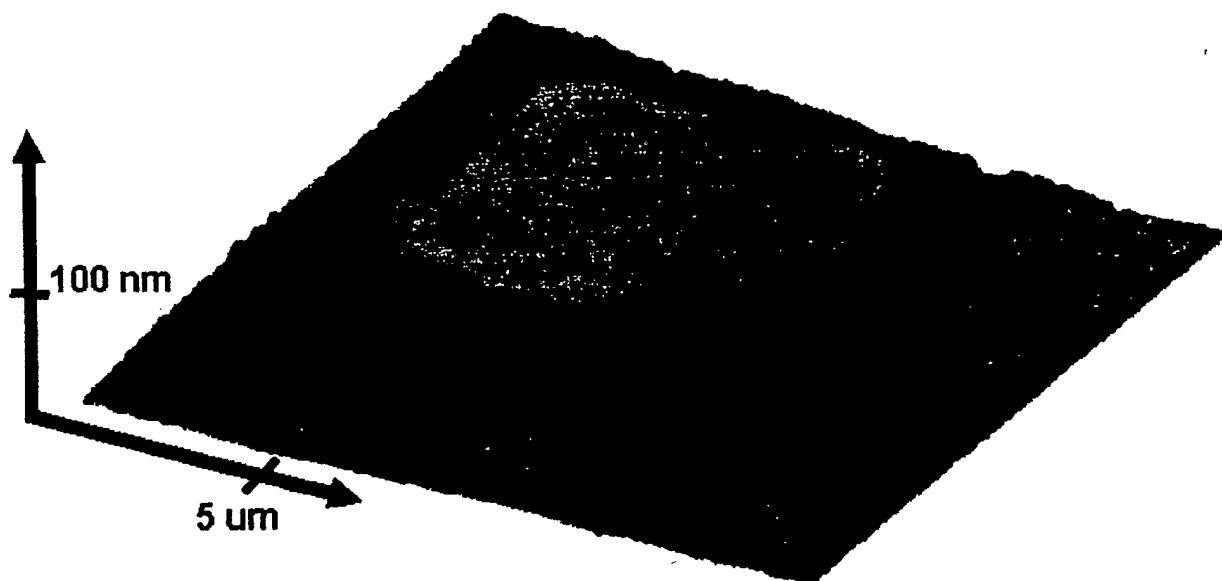


Fig. 7



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